



## Standard Specifications

Stamp	Turns (Ts)	Impedance (Ω)Min 25MHz	Impedance (Ω)Min 100MHz	IDC (A)Max $\Delta T \cong 40^\circ C$	Fig
S5H504655MTL-2R5T-TC050	2.5	204	311	7.0	1
S5H504685MTL-2R5T-TC050	2.5	326	435	7.0	1
S5H504685M1TL-2R5T-TC050	2.5	326	435	7.0	2

\*All the data listed in this catalogue are for reference only, JENG SHI reserves the right to alter or revise the specifications without prior notification.

\*Beyond the above specification also can meet the special requirements. Need detailed content Please to the website query or contact us.

